CLAIM AMENDMENTS:

1. (cancelled)

2. (currently amended) The method of ~~claim 1~~claim 3, wherein said detection window comprises an odd number of samples.

3. (currently amended) ~~The method of claim 1~~ A method for bit stream decoding of a received bit stream, the method comprising the steps of:

   a) defining a detection window of sample values which are used to determine a value of a bit cell in a bit stream;
   b) applying a majority voting to said sample values within said detection window;
   c) generating a value of said bit cell in dependence on the results of step b); and
   d) decoding the bit stream using the bit cell values generated in step c), wherein said detection window is positioned at an expected edge between two bit cells of the bit stream to overlap at least one sample of a first bit cell and at least one sample of a subsequent bit cell, having respective sample values, in order to perform bit edge detection.

4. (currently amended) The method of ~~claim 1~~claim 3, wherein said detection window is centered on an expected center of a bit cell of the bit stream to only overlap samples of said bit cell for detecting a bit value of said bit cell.

5. (original) The method of claim 4, wherein glitches or spikes in the bit stream are filtering out.

6. (currently amended) ~~The method of claim 4~~A method for bit stream decoding of a received bit stream, the method comprising the steps of:

a) defining a detection window of sample values which are used to determine a value of a bit cell in a bit stream;
b) applying a majority voting to said sample values within said detection window;
c) generating a value of said sampled bit cell in dependence on the results of step b); and
d) decoding the bit stream using the bit values generated in step c), wherein said detection window is positioned on an expected center of said bit cell of the bit stream in dependence on a predetermined offset-parameter and in dependence on a predetermined parameter specifying a number of samples in said detection window to only overlap samples of said bit cell for detecting a bit value of said bit cell.

7. (cancelled)

8. (cancelled)

9. (cancelled)

10. (currently amended) The device of ~~claim 9~~claim11, wherein said detection window comprises an odd number of samples.

11. (currently amended) ~~The device of claim 9~~A device for decoding a received bit stream, the device comprising:

means for defining a detection window of sample values which are used to determine a value of a bit cell in a bit stream;
means for applying majority voting to said sample values within said detection window;
means for generating a value of said sampled bit cell in dependence on results of said voting; and
means for decoding the bit steam using the generated bit cell values, wherein said detection window is positioned at an expected edge between two bit cells of the bit stream to overlap at least one sample of a first bit cell and at least one sample of a subsequent bit cell, having respective sample values, in order to perform bit edge detection.

12. (currently amended) The device of ~~claim 9~~claim 11, wherein said detection window is centered on an expected ~~center of a bit cell~~ edge between two bit cells of the bit stream to only overlap samples of said bit cell for detecting a bit value of said bit cell.

13. (original) The device of claim 12, wherein glitches or spikes in the bit stream are filtering out.

14. (currently amended) ~~The device of claim 12~~ A device for decoding a received bit stream, the device comprising:

means for defining a detection window of sample values which are used to determine a value of a bit cell in a bit stream;
means for applying majority voting to said sample values within said detection window;
means for generating a value of said sampled bit cell in dependence on results of said voting; and

means for decoding the bit steam using the generated bit cell values, wherein said detection window is positioned at an expected ~~center~~edge between two bit cells of the bit stream ~~of said bit cell~~ according to a predetermined offset-parameter and according to a predetermined parameter specifying a number of samples in said detection window to only overlap samples of said bit cell for detecting a bit value of said bit cell.

15. (currently amended) A node in a communication system, the node comprising the device of ~~claim 9~~claim 11.

16. (cancelled)

17. (currently amended) A ~~data storage~~computer readable medium having instructions ~~stored thereon which are~~ executable by at least one of a computer and a microprocessor stored thereon~~, the executable instructions comprising~~ for performing the steps of:

~~a) sampling a bit in a bit stream to generate at least two sample values of the bit;~~

~~b) defining a detecting window of sample values which are used to determine a value of the bit;~~

~~c) applying majority voting to the sample values within the detection window; and~~

~~d) generating a value of the sampled bit in dependence on results of step c).~~

a) defining a detection window of sample values which are used to determine a value of a bit cell in a bit stream;

b) applying a majority voting to said sample values within said detection window;

c) generating a value of said bit cell in dependence on the results of step b); and

d) decoding the bit stream using the bit cell values generated in step c), wherein said detection window is positioned at an expected edge between two bit cells of the bit stream to overlap at least one sample of a first bit cell and at least one sample of a subsequent bit cell, having respective sample values, in order to perform bit edge detection.

18. (new) The method of claim 3, including:

sampling said bit cell in the received bit stream, to generate at least two sample values of said bit cell.

19. (new) The method of claim 6, wherein said detection window comprises an odd number of samples.

20. (new) The method of claim 6, wherein said detection window is centered on an expected center of a bit cell of the bit stream to only overlap samples of said bit cell for detecting a bit value of said bit cell.

21. (new) The method of claim 19, wherein glitches or spikes in the bit stream are filtering out.

22. (new) The method of claim 6, including:

sampling said bit cell in the received bit stream, to generate at least two sample values of said bit cell.

23. (new) The device of claim 11, including a sampling unit for sampling said bit cell in the received bit stream to generate at least two sample values of said bit cell.

24. (new) The device of claim 14, wherein said detection window comprises an odd number of samples.

25. (new) The device of claim 14, wherein said detection window is positioned at an expected edge between two bit cells of the bit stream to only overlap samples of said bit cell for detecting a bit value of said bit cell.

26. (new) The device of claim 25, wherein glitches or spikes in the bit stream are filtering out.

27. (new) The device of claim 14, including a sampling unit for sampling said bit cell in the received bit stream to generate at least two sample values of said bit cell.

28. (new) A node in a communication system, including a device as claimed in claim 14.